

Figure 1 is a block diagram of a super-heterodyne radio receiver.

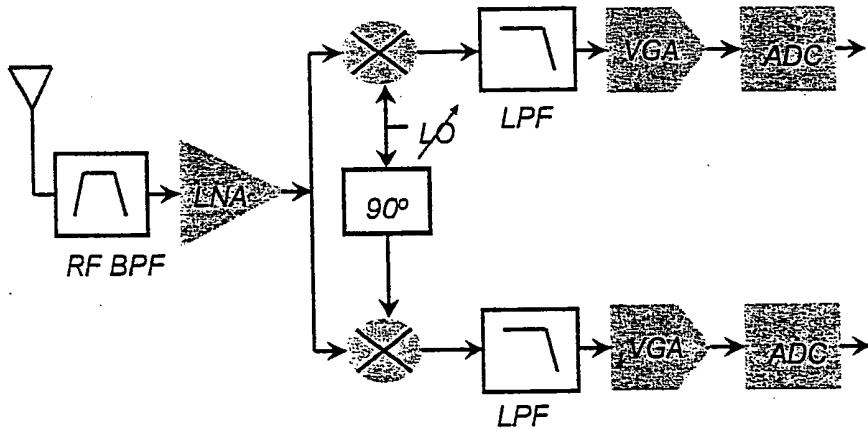


Figure 2 is a block diagram of a direct-conversion radio receiver.

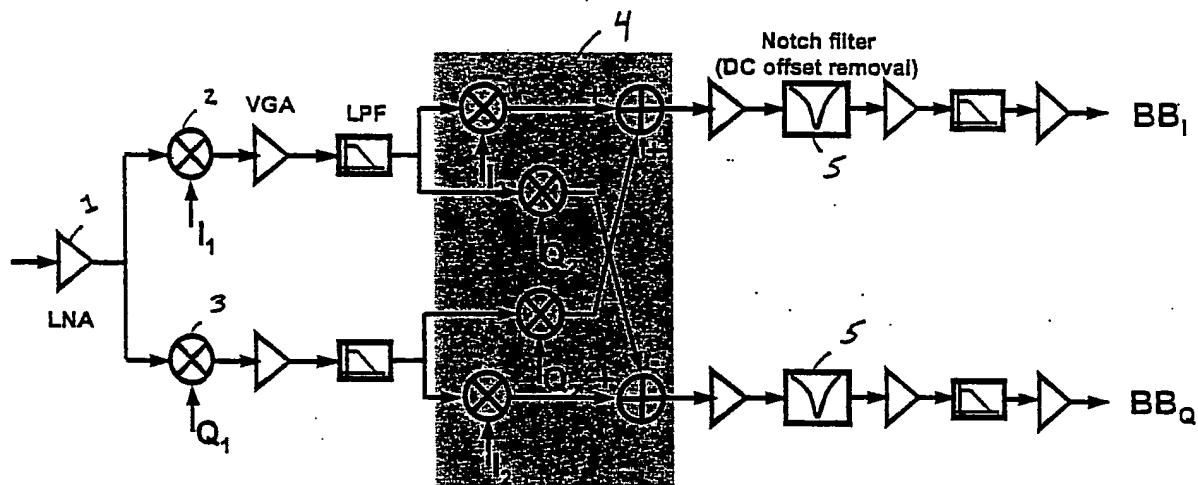


Figure 3 is a block diagram showing one embodiment of a receiver implemented in accordance with the present invention.

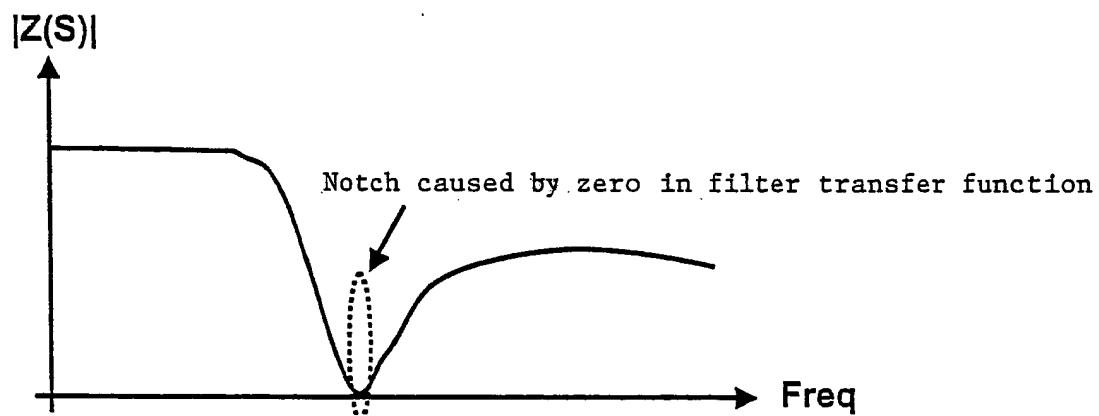


Figure 4 is a graph showing a transfer function of an elliptic filter which may be used as a notch filter in the receiver of the present invention.

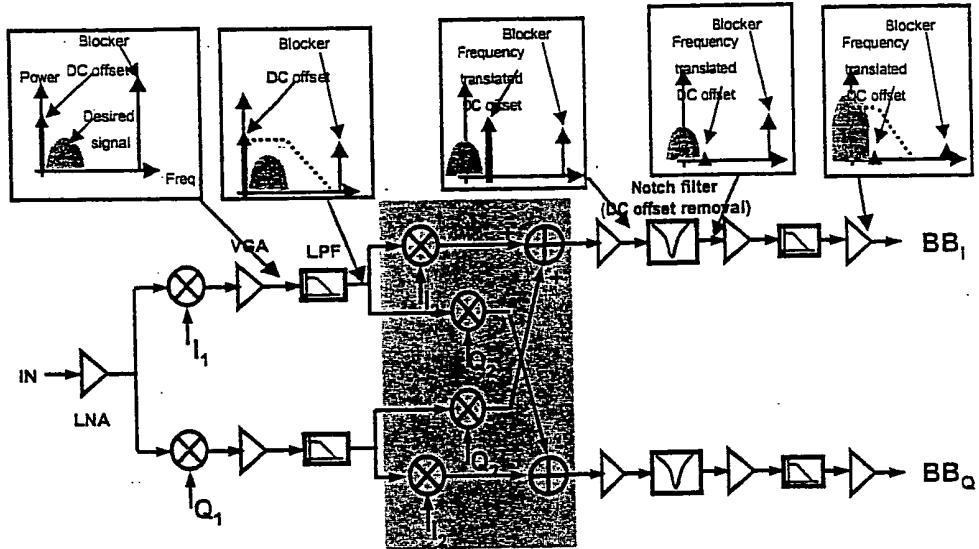


Figure 5 shows operating waveforms obtained at various stages within a receiver implemented in accordance with one embodiment of the present invention.

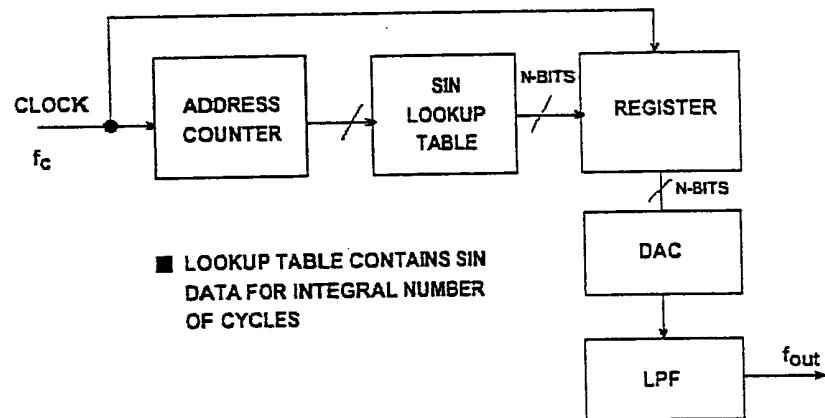


Figure 6 is a block diagram of a DDFS circuit which generates a second LO signal in accordance with one embodiment of the present invention.

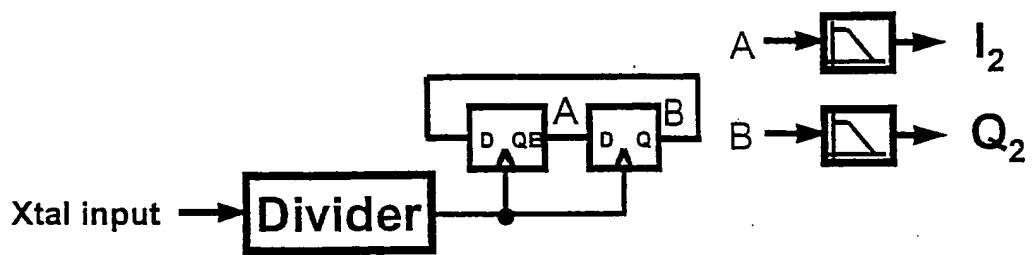


Figure 7 is a block diagram of another circuit which may be used to generate a second LO signal in accordance with the present invention.